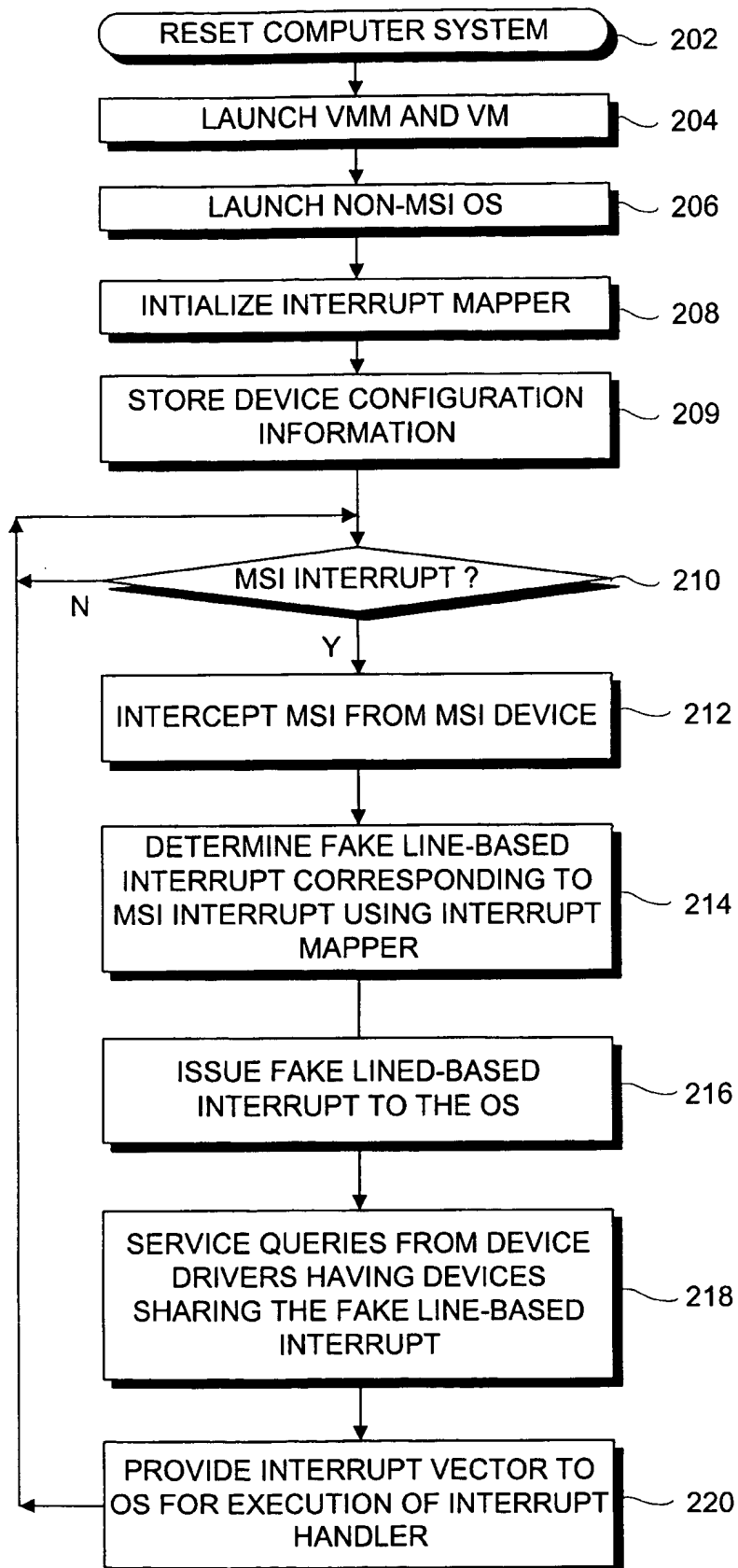


100

*Fig. 1*

**Fig. 2**



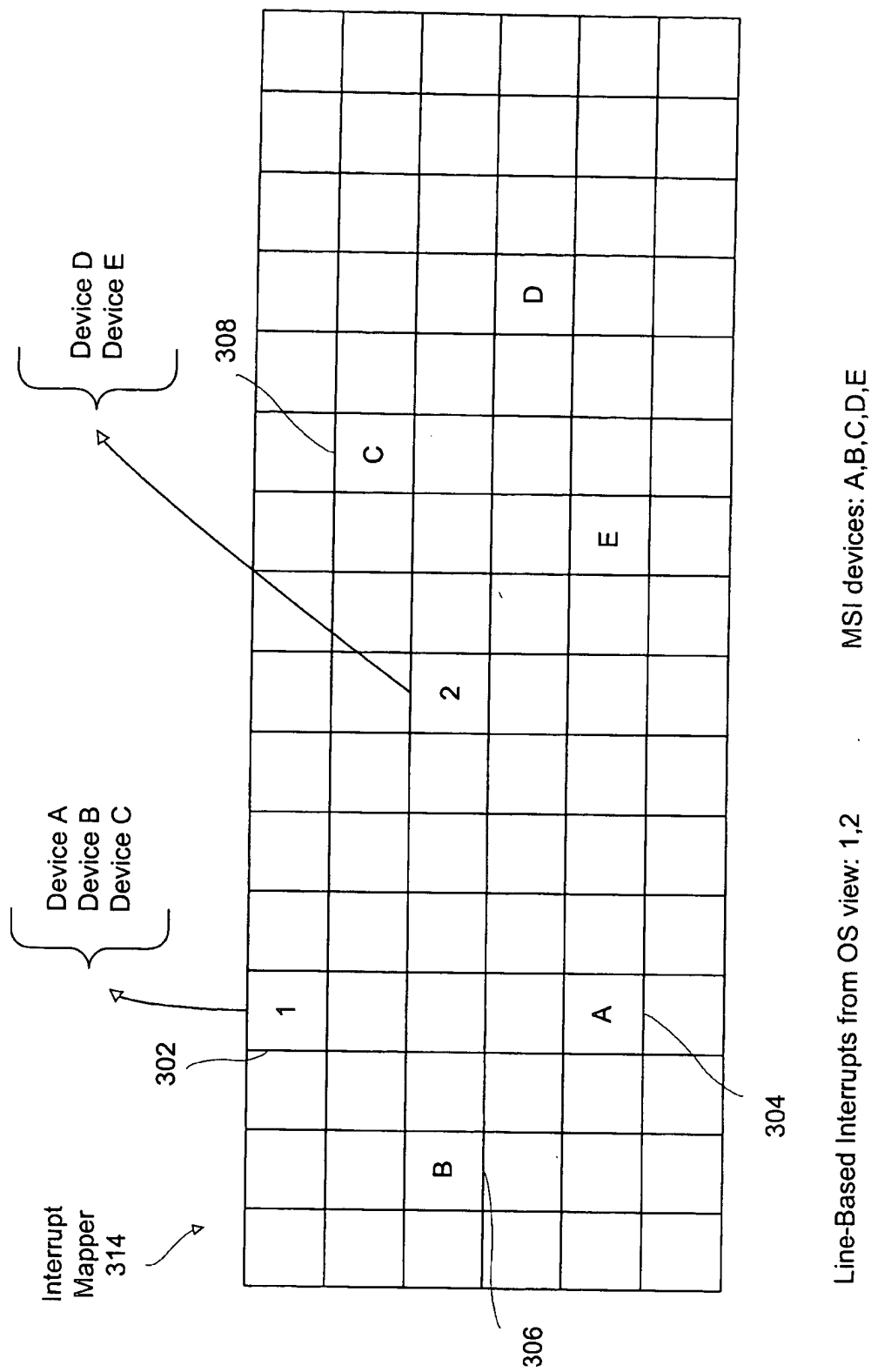
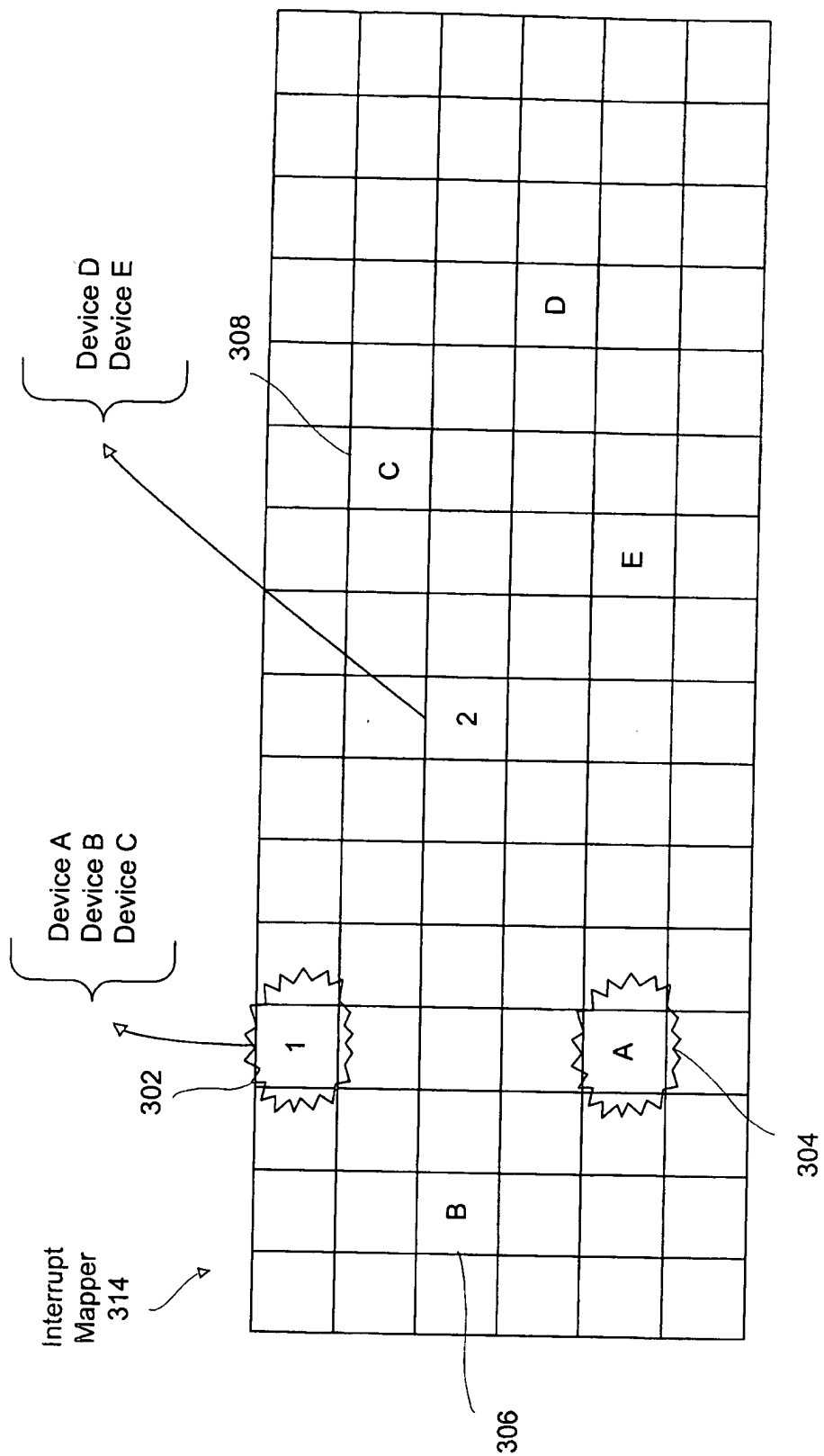


Fig. 3A



Line-Based Interrupts from OS view: 1,2

MSI devices: A,B,C,D,E

Fig. 3B

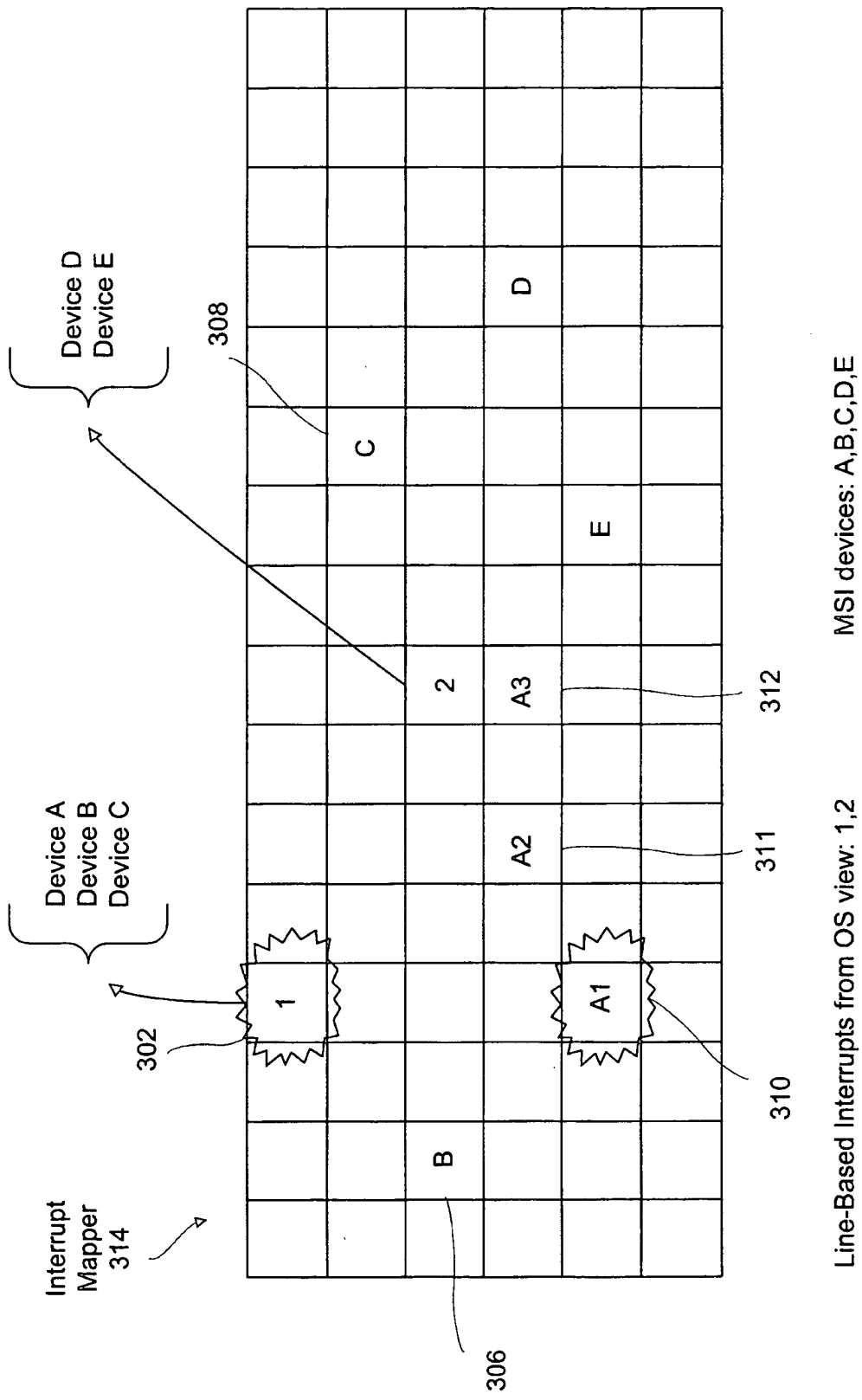
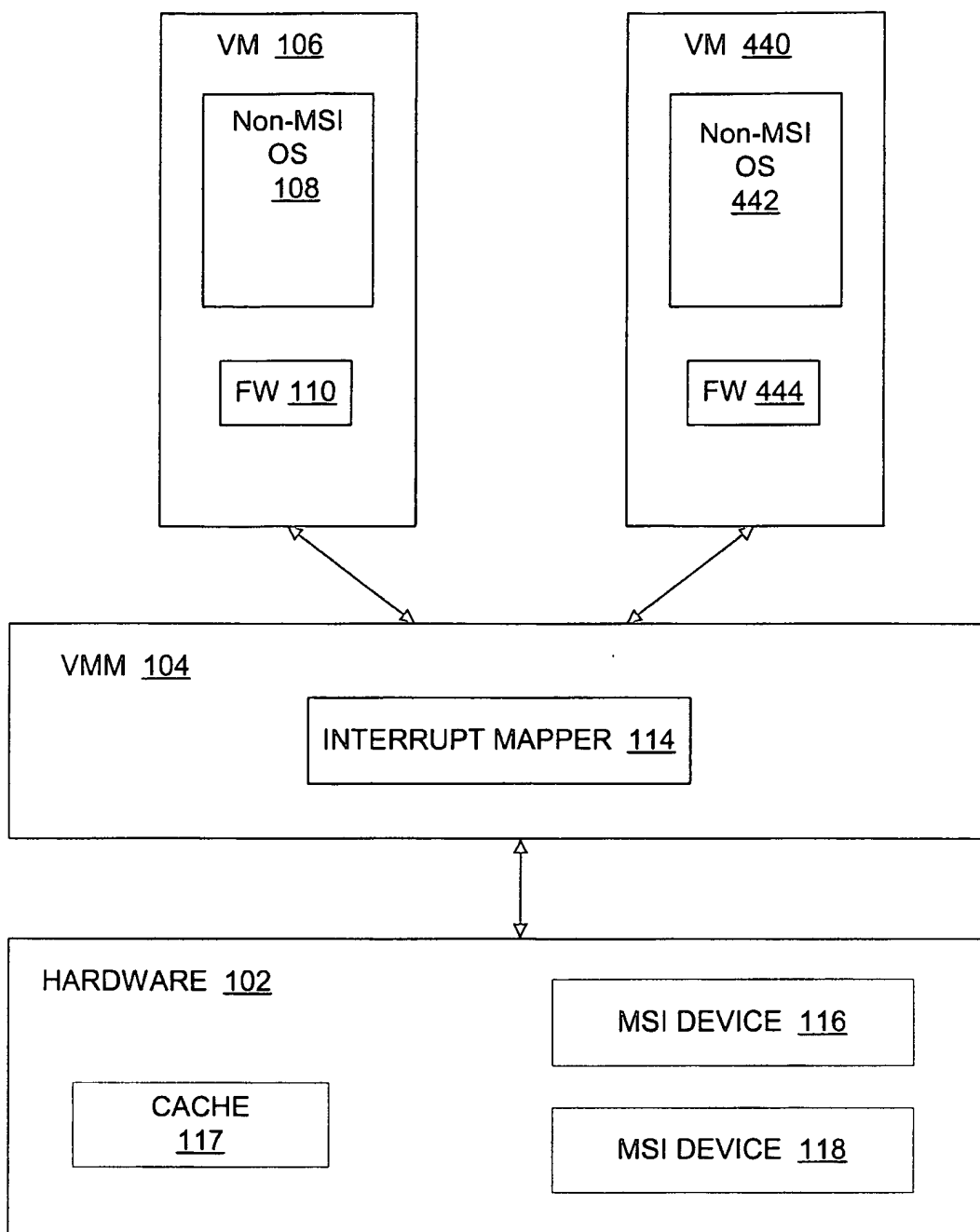
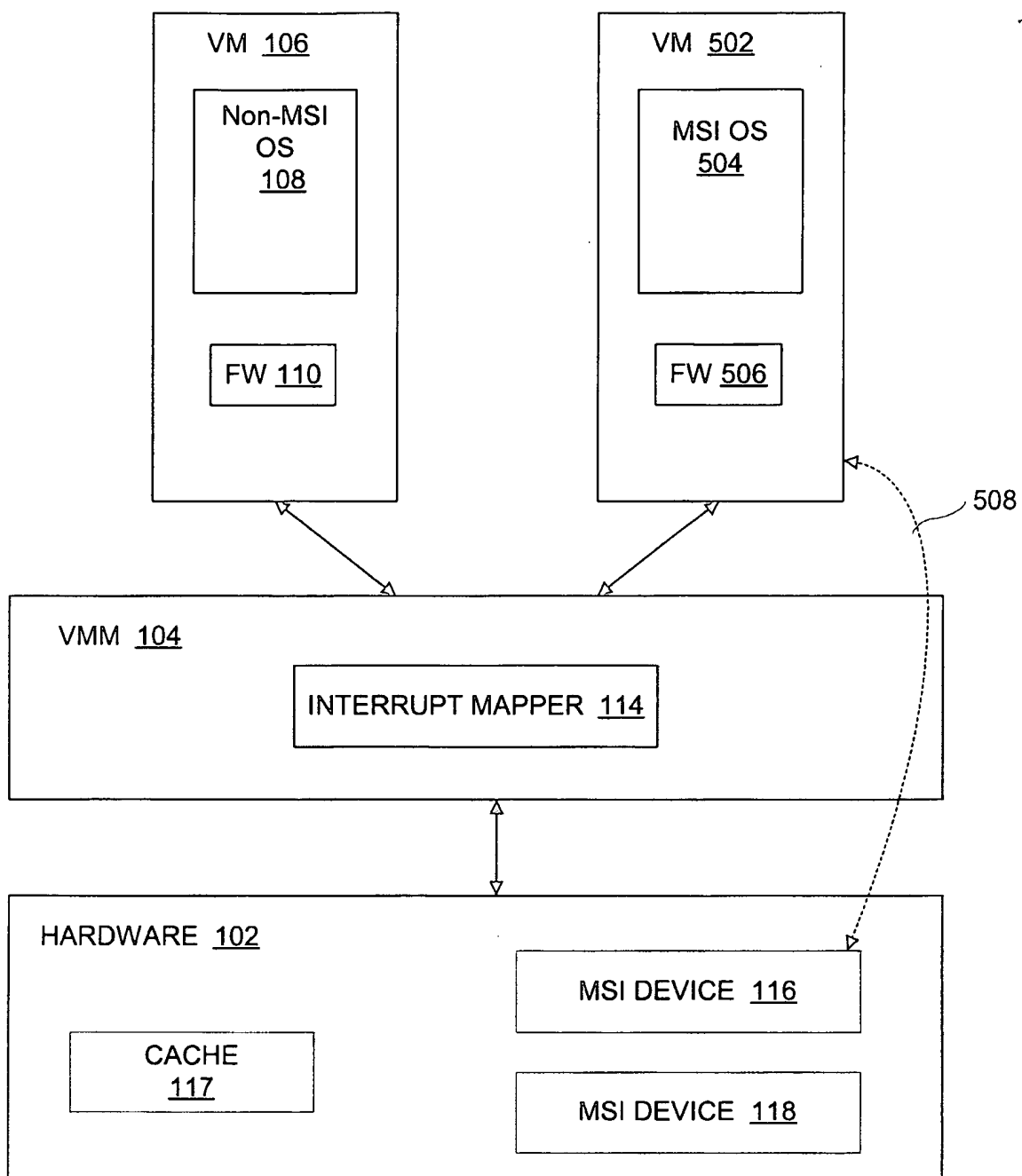


Fig. 3C



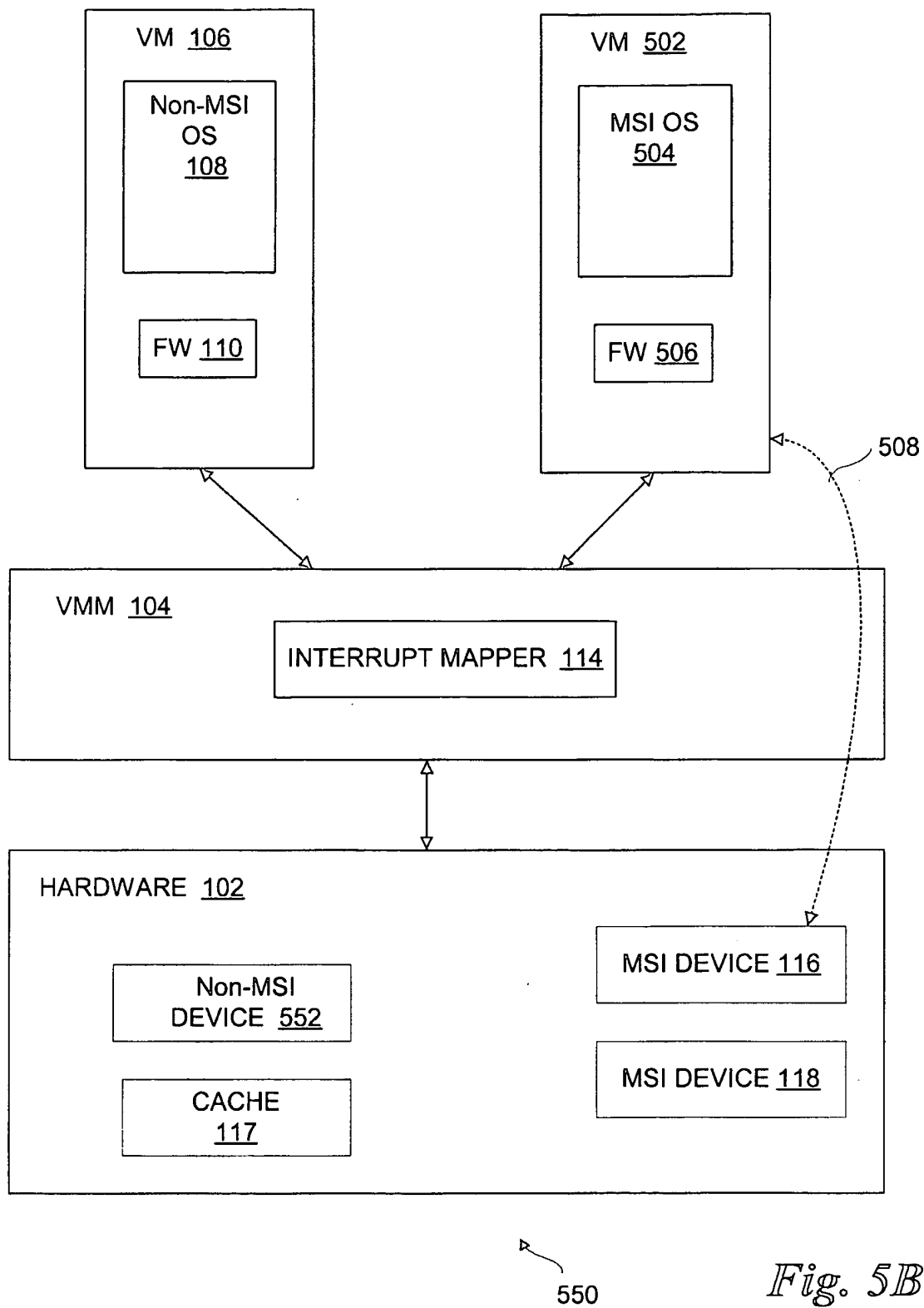
400

*Fig. 4*



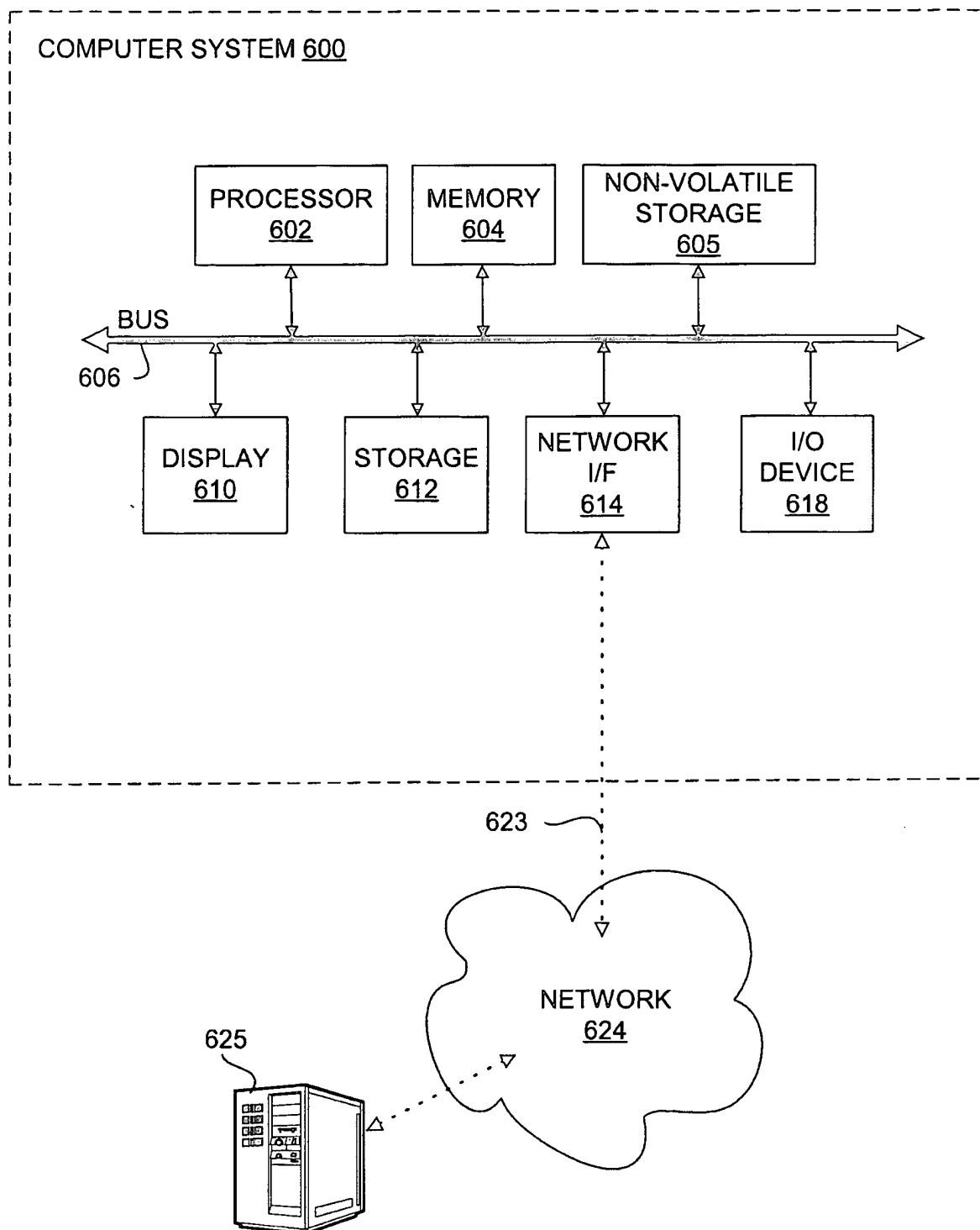
500

Fig. 5A



*Fig. 5B*





*Fig. 6*